

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	250	multiple adj bit adj per adj cell	US-PGPUB; USPAT	OR	ON	2006/04/06 09:20
L2	73	mbpc	US-PGPUB; USPAT	OR	ON	2006/04/06 09:20
L3	322	1 or 2	US-PGPUB; USPAT	OR	ON	2006/04/06 09:21
L4	39	wong-sau-ching.in.	US-PGPUB; USPAT	OR	ON	2006/04/06 09:21

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Day : Thursday

Date: 4/6/2006
Time: 09:31:08

PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = WONG

First Name = SAU

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>09518608</u>	<u>6662263</u>	150	03/03/2000	SECTORLESS FLASH MEMORY ARCHITECTURE	WONG, SAU C.
<u>09670764</u>	<u>6314025</u>	150	09/27/2000	High data rate write process for non-volatile flash memories	WONG, SAU C.
<u>09680797</u>	<u>6307776</u>	150	10/06/2000	Multi-bit-per-cell flash EEPROM memory with refresh	WONG, SAU C.
<u>09768922</u>	<u>6330185</u>	150	01/23/2001	High bandwidth multi-level flash memory using dummy memory accesses to improve precision when writing or reading a data stream	WONG, SAU C.
<u>09967513</u>	<u>6456529</u>	150	09/27/2001	PROGRAMMABLE IMPEDANCE DEVICE	WONG, SAU C.
<u>10042504</u>	<u>6606267</u>	150	10/18/2001	HIGH DATA RATE WRITE PROCESS FOR NON-VOLATILE FLASH MEMORIES	WONG, SAU C.
<u>10045505</u>	<u>6898117</u>	150	10/18/2001	MULTI-BIT-PER-CELL FLASH EEPROM MEMORY WITH REFRESH	WONG, SAU C.
<u>10109353</u>	<u>6556465</u>	150	03/27/2002	ADJUSTABLE CIRCUITS FOR ANALOG OR MULTI-LEVEL MEMORY	WONG, SAU C.
<u>10264970</u>	<u>6624773</u>	150	10/04/2002	DATA ENCRYPTION AND SIGNAL SCRAMBLING USING PROGRAMMABLE DATA CONVERSION ARRAYS	WONG, SAU C.
<u>10359476</u>	Not Issued	93	02/05/2003	INTEGRATED CIRCUIT WITH ANALOG OR MULTILEVEL STORAGE CELLS AND USER-SELECTABLE SAMPLING FREQUENCY	WONG, SAU C.
<u>10382333</u>	<u>6760262</u>	150	03/04/2003	ADJUSTABLE CHARGE PUMP	WONG, SAU C.

				CIRCUIT ADJUSTABLE IN RESPONSE TO AN EXTERNAL VOLTAGE SOURCE	
<u>10461851</u>	<u>6944058</u>	150	06/13/2003	HIGH DATA RATE WRITE PROCESS FOR NON-VOLATILE FLASH MEMORIES	WONG, SAU C.
<u>10678885</u>	Not Issued	93	10/02/2003	FILE STORAGE AND ERASURE IN FLASH MEMORY	WONG, SAU C.
<u>11101938</u>	Not Issued	71	04/07/2005	Multi-bit-per-cell flash EEPROM memory with refresh	WONG, SAU C.
<u>11184322</u>	Not Issued	30	07/18/2005	High data rate write process for non-volatile flash memories	WONG, SAU C.
<u>08333381</u>	<u>5694356</u>	150	11/02/1994	HIGH RESOLUTION ANALOG STORAGE EPROM AND FLASH EPROM	WONG, SAU C.
<u>08535441</u>	<u>5745409</u>	150	09/28/1995	NON-VOLATILE MEMORY WITH ANALOG AND DIGITAL INTERFACE AND STORAGE	WONG, SAU C.
<u>08583905</u>	<u>5687115</u>	150	01/11/1996	WRITE CIRCUITS FOR ANALOG MEMORY FOR FLOATING GATE EEPROM MEMORY	WONG, SAU C.
<u>08585072</u>	<u>5751635</u>	150	01/11/1996	READ CIRCUITS FOR ANALOG MEMORY CELLS	WONG, SAU C.
<u>08585138</u>	<u>5638320</u>	150	01/11/1996	HIGH RESOLUTION ANALOG STORAGE EPROM AND FLASH EPROM	WONG, SAU C.
<u>08587310</u>	<u>5680341</u>	150	01/16/1996	PIPELINED RECORD AND PLAYBACK FOR ANALOG NON-VOLATILE MEMORY	WONG, SAU C.
<u>08598485</u>	<u>5682352</u>	150	02/08/1996	DIGITAL TESTING OF ANALOG MEMORY DEVICES	WONG, SAU C.
<u>08622332</u>	<u>5748533</u>	150	03/26/1996	READ CIRCUIT WHICH USES A COARSE-TO-FINE SEARCH WHEN READING THE THRESHOLD VOLTAGE OF A MEMORY CELL	WONG, SAU C.
<u>08622333</u>	<u>5748534</u>	150	03/26/1996	FEEDBACK LOOP FOR READING THRESHOLD VOLTAGE	WONG, SAU C.
<u>08684788</u>	<u>5818757</u>	150	07/22/1996	ANALOG AND MULTI-LEVEL MEMORY WITH REDUCED PROGRAM DISTURB	WONG, SAU C.

<u>08781741</u>	<u>5923585</u>	150	01/10/1997	SOURCE BIASING IN NON-VOLATILE MEMORY HAVING ROW-BASED SECTORS	WONG, SAU C.
<u>08839288</u>	<u>5949716</u>	150	04/16/1997	LOOK-AHEAD ERASE FOR SEQUENTIAL DATA STORAGE	WONG, SAU C.
<u>08847234</u>	<u>5815425</u>	150	04/30/1997	COMBINED DIGITAL WRITE AND ANALOG REWRITE PROCESS FOR NON-VOLATILE MEMORY	WONG, SAU C.
<u>08847236</u>	<u>5801980</u>	150	05/01/1997	NON-VOLATILE MEMORY WITH ANALOG AND DIGITAL INTERFACE AND STORAGE	WONG, SAU C.
<u>08867473</u>	<u>5909387</u>	150	06/02/1997	MEMORY ARCHITECTURE FOR RECORDING OF MULTIPLE MESSAGES	WONG, SAU C.
<u>08889111</u>	<u>5896340</u>	150	07/07/1997	MULTIPLE ARRAY ARCHITECTURE FOR ANALOG OR MULTI-BIT-PER-CELL MEMORY	WONG, SAU C.
<u>08924909</u>	<u>5909449</u>	150	09/08/1997	MULTIBIT-PER-CELL NON-VOLATILE MEMORY WITH ERROR DETECTION AND CORRECTION	WONG, SAU C.
<u>09053716</u>	<u>6038166</u>	150	04/01/1998	HIGH RESOLUTION MULTI-BIT-PER-CELL MEMORY	WONG, SAU C.
<u>09086785</u>	<u>6594036</u>	150	05/28/1998	ANALOG/MULTI-LEVEL MEMORY FOR DIGITAL IMAGING	WONG, SAU C.
<u>09103623</u>	<u>5969986</u>	150	06/23/1998	HIGH-BANDWIDTH READ AND WRITE ARCHITECTURES FOR NON-VOLATILE MEMORIES	WONG, SAU C.
<u>09128225</u>	<u>6134145</u>	150	08/03/1998	HIGH DATA RATE WRITE PROCESS FOR NON-VOLATILE FLASH MEMORIES	WONG, SAU C.
<u>09159397</u>	Not Issued	164	09/23/1998	ANALOG BUFFER MEMORY FOR HIGH-SPEED DIGITAL IMAGE CAPTURE	WONG, SAU C.
<u>09159704</u>	<u>6169503</u>	150	09/23/1998	PROGRAMMABLE ARRAYS FOR DATA CONVERSIONS BETWEEN ANALOG AND DIGITAL	WONG, SAU C.
<u>09159848</u>	<u>6201734</u>	150	09/25/1998	PROGRAMMABLE IMPEDANCE DEVICE	WONG, SAU C.

<u>09199971</u>	<u>6345000</u>	150	11/25/1998	FLASH MEMORY PERMITTING SIMULTANEOUS READ/WRITE AND ERASE OPERATIONS IN A SINGLE MEMORY ARRAY	WONG, SAU C.
<u>09200205</u>	<u>6154157</u>	150	11/25/1998	NON-LINEAR MAPPING OF THRESHOLD VOLTAGES FOR ANALOG/MULTI-LEVEL MEMORY	WONG, SAU C.
<u>09200220</u>	<u>6151246</u>	150	11/25/1998	MULTI-BIT-PER-CELL FLASH EEPROM MEMORY WITH REFRESH	WONG, SAU C.
<u>09224168</u>	<u>6760068</u>	150	12/31/1998	CORRECTION OF CORRUPTED ELEMENTS IN SENSORS USING ANALOG/MULTI-LEVEL NON-VOLATILE MEMORY	WONG, SAU C.
<u>09224183</u>	<u>6058060</u>	150	12/31/1998	MULTI-BIT-PER-CELL AND ANALOG/MULTI-LEVEL NON-VOLATILE MEMORIES WITH IMPROVED RESOLUTION AND SIGNAL-TO NOISE RATIO	WONG, SAU C.
<u>09293133</u>	<u>6160739</u>	150	04/16/1999	NON-VOLATILE MEMORIES WITH IMPROVED ENDURANCE AND EXTENDED LIFETIME	WONG, SAU C.
<u>09305512</u>	Not Issued	161	05/05/1999	MULTI-BIT-PER-CELL AND ANALOG/MULTI-LEVEL NON-VOLATILE MEMORIES WITH IMPROVED RESOLUTION AND SIGNAL-TO-NOISE RATIO	WONG, SAU C.
<u>09305544</u>	<u>6285593</u>	150	05/05/1999	WORD-LINE DECODER FOR MULTI-BIT-PER-CELL AND ANALOG/MULTI-LEVEL MEMORIES WITH IMPROVED RESOLUTION AND SIGNAL-TO-NOISE RATIO	WONG, SAU C.
<u>09324902</u>	<u>6208542</u>	150	06/02/1999	TECHNIQUES FOR STORING DIGITAL DATA IN AN ANALOG OR MULTILEVEL MEMORY	WONG, SAU C.
<u>09342900</u>	<u>6184726</u>	150	06/29/1999	ADJUSTABLE LEVEL SHIFTER CIRCUITS FOR ANALOG OR MULTILEVEL MEMORIES	WONG, SAU C.
<u>09343117</u>	<u>6549456</u>	150	06/29/1999	INTEGRATED CIRCUIT WITH ANALOG OR MULTILEVEL STORAGE CELLS AND USER-	WONG, SAU C.

				SELECTABLE SAMPLING FREQUENCY	
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